

FIG. 1

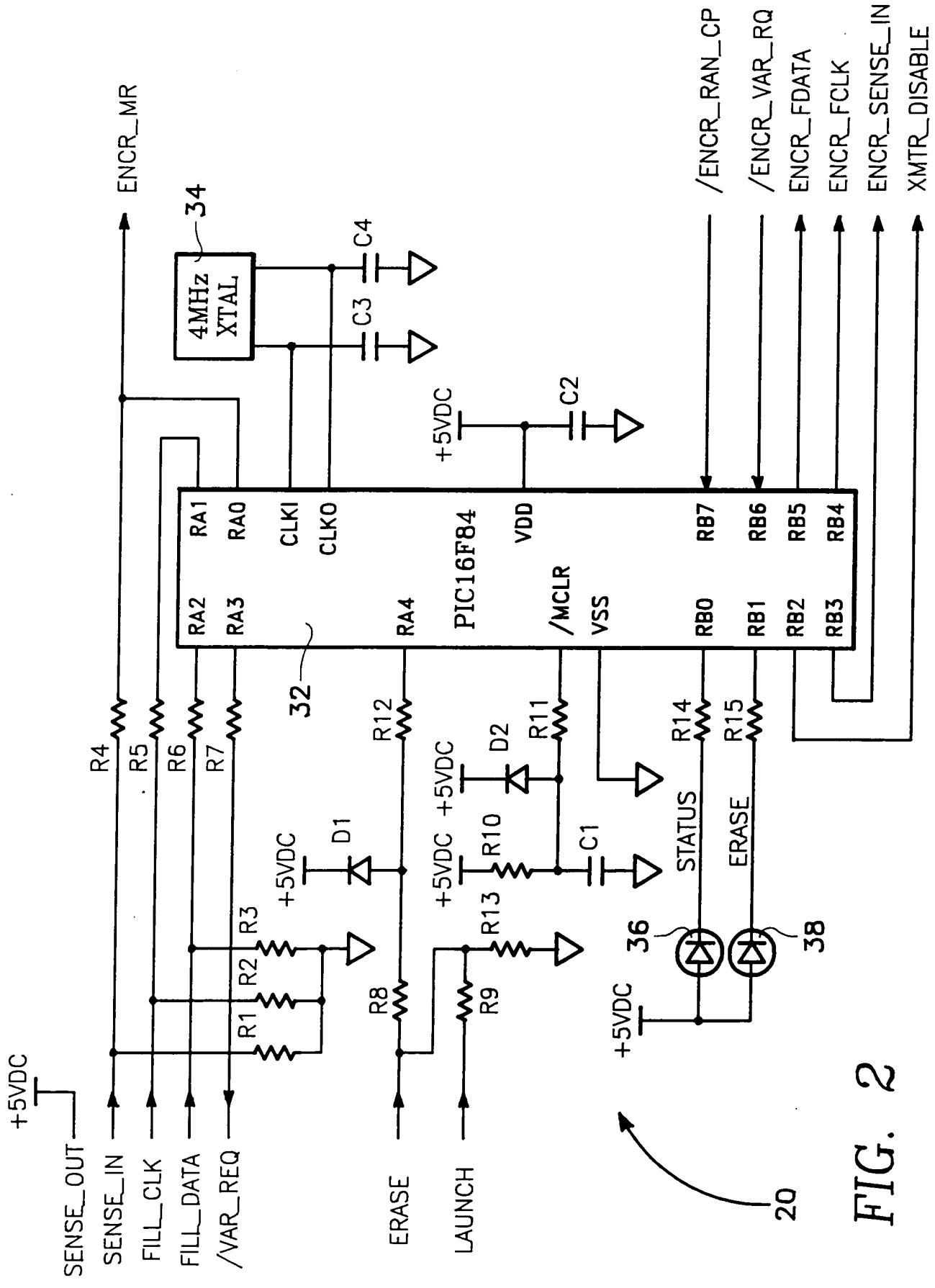


FIG. 2

REQUEST (OUT)

FIG. 3A

t_{RF}

DATA (IN)

10%

FIG. 3B

90% t_{DC}

CLOCK (OUT)

T

t_{CR}

t_{DD}

t_{DC}

t_{C11}

FIG. 3C

50%
10% t_{CR}
 t_{CL}

t_{CL}

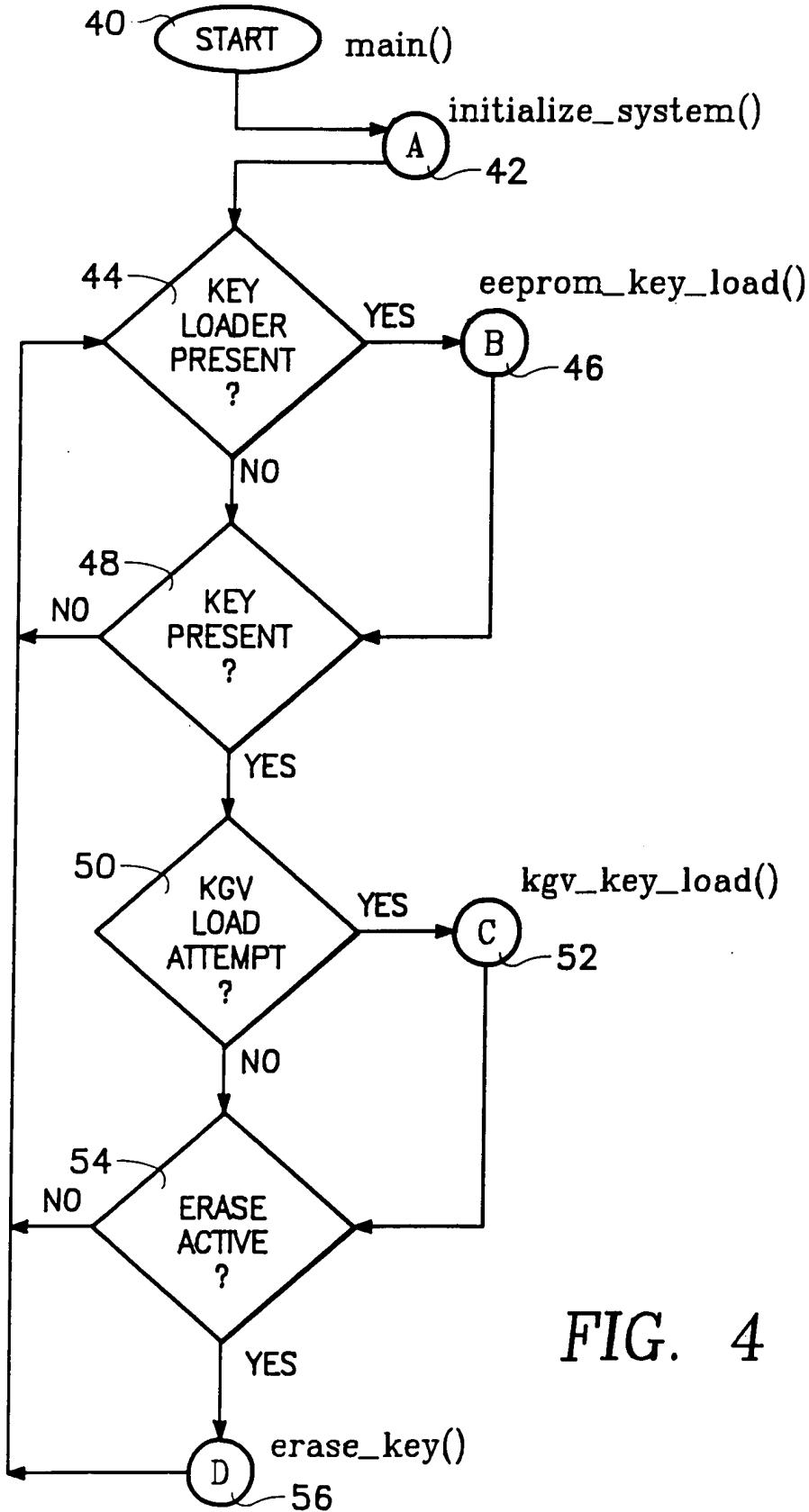
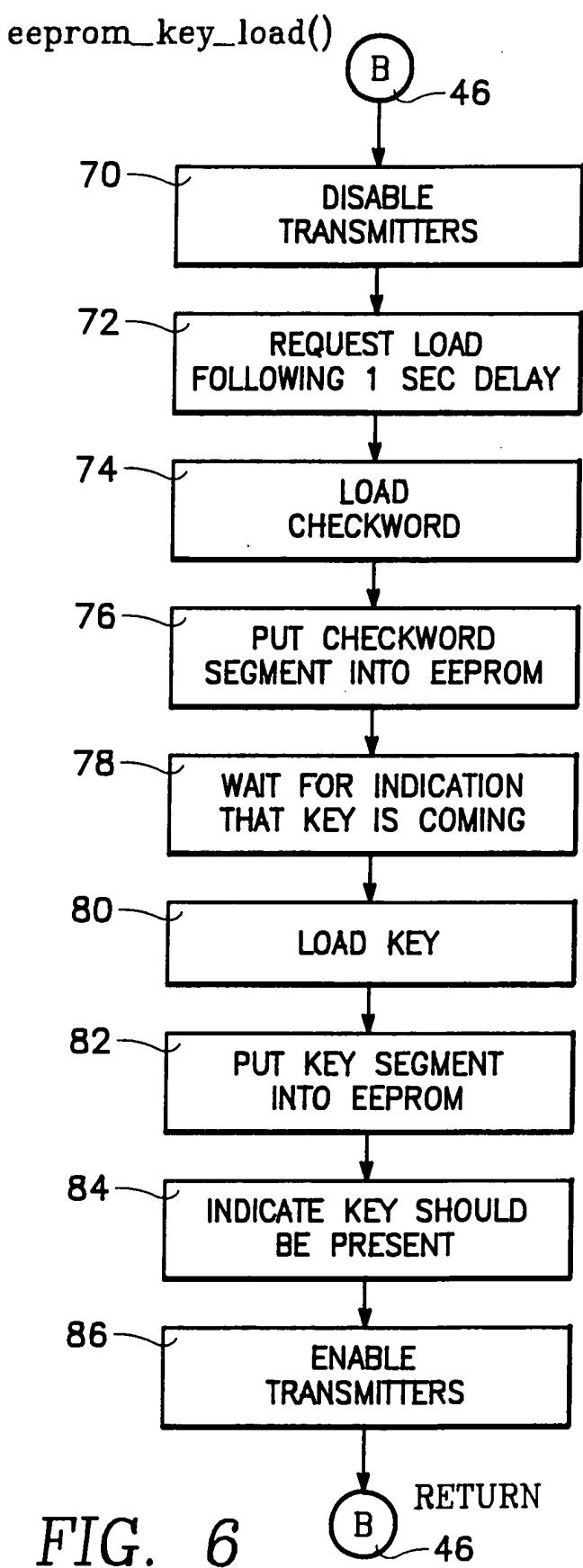
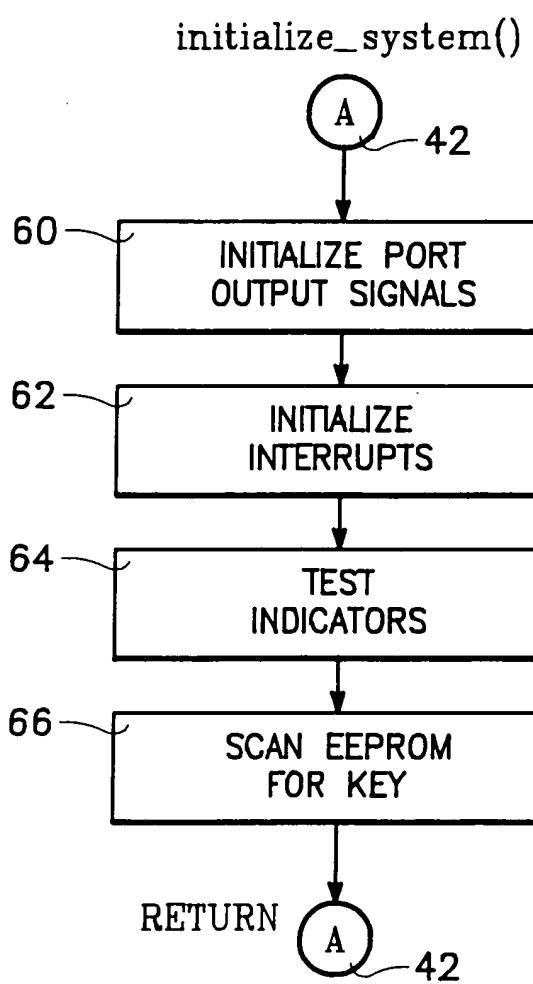


FIG. 4



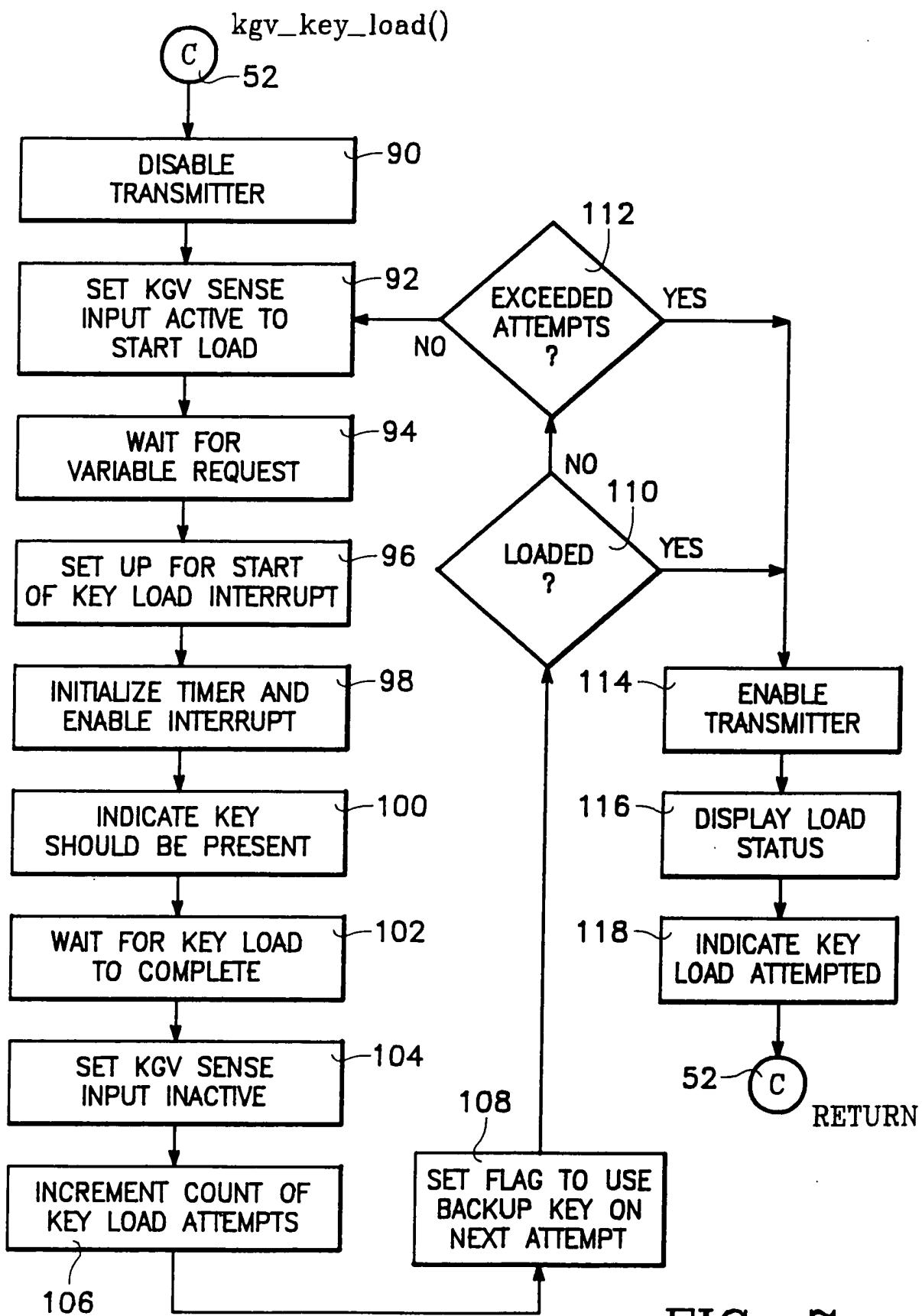


FIG. 7

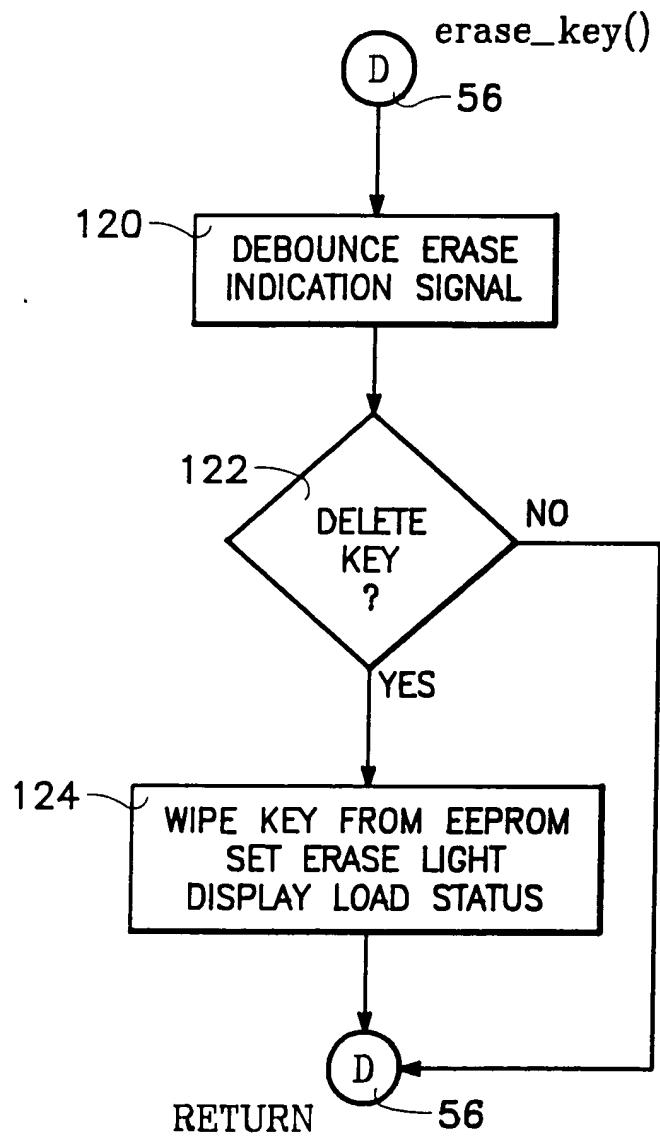
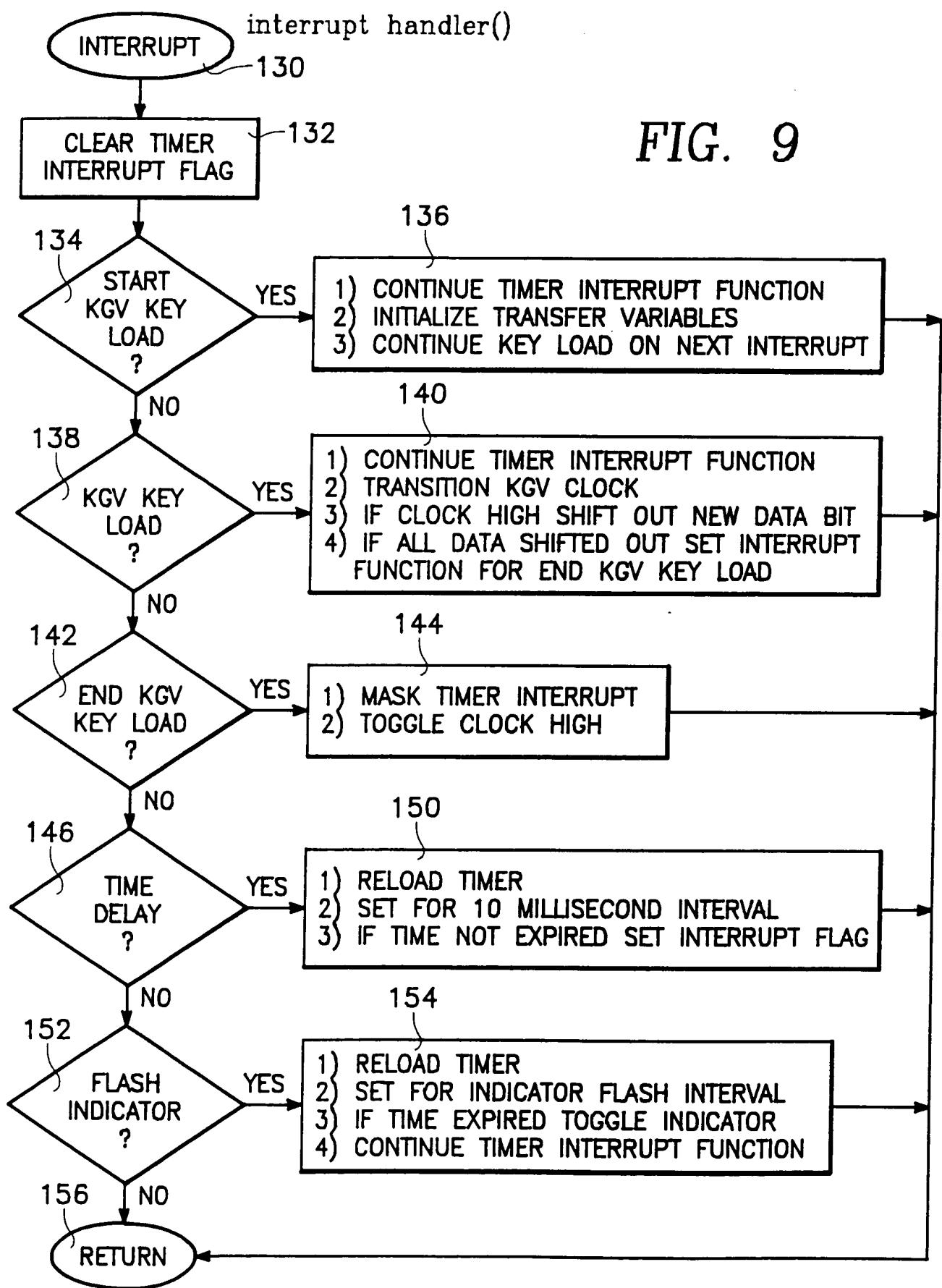


FIG. 8

FIG. 9



00000000000000000000000000000000